

## Digital Circuits

- The Duality principle:

To obtain the dual of a logic expression: Replace - by,++ by $\cdot, 0$ by 1 , and 1 by 0 , but do not change the variables.

$$
a+b+0 \ldots \Leftrightarrow a \cdot b \cdot 1 \ldots
$$

Example: The dual of the expression $a+a \cdot b$ is $a \cdot(a+b)$.
Principle: Duals of all proven theorems are also theorems.
Given a Boolean algebra identity, another identity can be obtained by taking the dual of both sides of the identity.

Note that in the previous slide, axioms were presented with their duals (in two columns).

## Example:

Absorption theorem (given in the next slide):
If we can prove the theorem $\mathrm{a}+\mathrm{a} \cdot \mathrm{b}=\mathrm{a}$, then its dual $\mathrm{a} \cdot(\mathrm{a}+\mathrm{b})=\mathrm{a}$ is also true.
General Duality Principle:
$\mathrm{f}(\mathrm{X} 1, \mathrm{X} 2, \ldots, \mathrm{Xn}, 0,1,+, \bullet) \Leftrightarrow \mathrm{f}^{\mathrm{D}}(\mathrm{X} 1, \mathrm{X} 2, \ldots, \mathrm{Xn}, 1,0, \cdot,+)$

- Duality establishes a relationship between proofs of theorems.
- Duals are not equal.


## Digital Circuits

## Theorems:

These theorems are derived from the operations and axioms of Boolean algebra. They can be proven using the axioms.

1. Annihilator (or Dominance):
$a+1=1$

$$
a \cdot 0=0
$$

2. Involution:
$\left(\mathrm{a}^{\prime}\right)^{\prime}=\mathrm{a}$ or $\overline{\overline{\mathrm{a}}}=\mathrm{a}$
3. Idempotency:

$$
a+a+a+\ldots+a=a \quad a \cdot a \cdot a \cdot \ldots \cdot a=a
$$

4. Absorption:
(Proof in 2.4)
$a+a \cdot b=a$

$$
a \cdot(a+b)=a
$$

5. De Morgan's Theorem: Augustus De Morgan, British mathematician and logician (1806-1871)
$\overline{(a+b)}=\bar{a} \cdot \bar{b}$
$\overline{(a \cdot b)}=\bar{a}+\bar{b}$
6. General form of De Morgan's Theorem:
$\mathrm{f} \overline{(\mathrm{X} 1, \mathrm{X} 2, \ldots, \mathrm{Xn}, 0,1,+, \cdot)}=\mathrm{g}(\overline{\mathrm{X}} 1, \overline{\mathrm{X}} 2, \ldots, \overline{\mathrm{Xn}}, 1,0, \cdot,+)$

- It establishes a relationship between AND and OR (• and +).
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## Digital Circuits

## Proving the theorems:

a) Using Axioms

## Example:

| Theorem: | $X \cdot Y+X \cdot \bar{Y}=X$ |
| :--- | :--- |
| Proof: |  |
| Distributive | $X \cdot Y+X \cdot \bar{Y}=X \cdot(Y+\bar{Y})$ |
| Inverse (Complement) | $X \cdot(Y+\bar{Y})$ |
| Identity | $X \cdot(1)$ |

## Example:

Theorem: $X+X \cdot Y=X$ (Absorption)
Proof:
Identity Distributive Annihilator
$X \cdot 1+X \cdot Y=X \cdot(1+Y)$

Identity
$X \cdot(1+Y)=X \cdot(1)$
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$X \cdot(1)$
$=X \checkmark$

## Digital Circuits

Proving the theorems: b) Using truth tables
Note that to denote negation (NOT), we can also use the notation $\overline{\mathrm{A}}$.

De Morgan:

$$
\overline{(X+Y)}=\bar{X} \cdot \bar{Y}
$$

| X | Y | $\overline{\mathrm{X}}$ | $\overline{\mathrm{Y}}$ | $\overline{(\mathrm{X}+\mathrm{Y})}$ | $\overline{\mathrm{X}} \cdot \overline{\mathrm{Y}}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 1 | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 0 | 0 |
| 1 | 0 | 0 | 1 | 0 | 0 |
| 1 | 1 | 0 | 0 | 0 | 0 |

$$
\overline{(X \bullet Y)}=\bar{X}+\bar{Y}
$$

| X | Y | $\overline{\mathrm{X}}$ | $\overline{\mathrm{Y}}$ | $\overline{(\mathrm{X} \cdot \mathrm{Y})}$ | $\overline{\mathrm{X}}+\overline{\mathrm{Y}}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 1 | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 1 | 1 |
| 1 | 0 | 0 | 1 | 1 | 1 |
| 1 | 1 | 0 | 0 | 0 | 0 |

Although truth tables may contain many rows, computer programs can fill in and compare these tables very quickly.

## Digital Circuits

Minimizing logic expressions using axioms and theorems:
Minimizing a logic expression means:

- finding the shortest expression
- with the fewest operations and variables
- that generates the same output values as the original expression.


## Example:

$$
Z(A, B, C)=\underbrace{\bar{A}}_{-} \cdot B \cdot C+\underbrace{}_{-} \cdot \bar{B} \cdot C+A_{-} \cdot B \cdot \bar{C}+A \cdot B \cdot C
$$

$$
=\bar{A} B C+A \bar{B} C+A B \bar{C}+A B C+A B C
$$

$$
=\bar{A} B C+A B C+A \bar{B} C+A B \bar{C}+A B C
$$

$$
=(\bar{A}+A) B C+A \bar{B} C+\underset{A}{ } \bar{B} \bar{C}+A B C
$$

$$
=(1) B C+A \bar{B} C+A B \bar{C}+A B C
$$

$$
=B C+A \bar{B} C+A B \bar{C}+A B C+A B C
$$

$$
=B C+A \bar{B} C+A B C+A B \bar{C}+A B C
$$

$$
=B C+A(\bar{B}+B) C+\underset{A}{ } B \bar{C}+A B C
$$

$$
=B C+A(1) C+A B \bar{C}+A B C
$$

$$
=B C+A C+A B(\bar{C}+C)
$$

$$
=B C+A C+A B(1)
$$

$$
=\mathbf{B} C+A C+A B
$$

## Logic (Boolean) Expressions

A logic expression is a finite combination of variables, constants, and operators that are well-formed according to the rules.
It is represented as $E(X)$, where $X=\left(x_{1}, x_{2}, \ldots . x_{n}\right) \in B^{n}$ and each $x_{i} \in\{0,1\}$. $B^{n}$ is the set of vectors with $n$ binary variables.

## Examples:

$$
\begin{aligned}
& \mathrm{E}\left(x_{1}, x_{2}, x_{3}, x_{4}\right)=x_{2} \overline{x_{3}}+x_{1} x_{4}+\overline{x_{1}} x_{2} \\
& \mathrm{E}(a, b, c)=a \bar{c}+a b \\
& \mathrm{E}(a, b, c, d)=(a+b+\bar{c})(\bar{a}+d)(b+\bar{d})
\end{aligned}
$$

## Literal:

In a Boolean expression, each separate occurrence of a variable, either in normal (uncomplemented) or inverse (complemented) form, is a literal.
For example, the expression $\mathrm{E}(a, b, c)=a \bar{c}+a b$ has three variables $(a, b, c)$ and contains four literals $(a \bar{c}+a b)$.
However, two of the literals are identical (a appears twice).

If $\mathrm{E}_{1}$ and $\mathrm{E}_{2}$ are logic expressions, then $\overline{E_{1}}, \overline{E_{2}}, E_{1}+E_{2}, E_{1} \cdot E_{2}$, and all possible combinations are also logic expressions.

## Digital Circuits

Normal forms of logic expressions:
Each logic expression can be written in two special forms.

1. Disjunctive normal form (DNF): $\Sigma \Pi$

Logic sum of logic products (SOP). OR of ANDs.
Example: $b \bar{c}+a d+\bar{a} b$
The OR (logic sum) operation is also called logical disjunction.
2. Conjunctive normal form (CNF): $\Pi \Sigma$

Logic product of logic sums (POS). AND of ORs.
Example: $(a+b+\bar{c})(a+d)(\bar{a}+b)$
The AND (logic product) operation is also called logical conjunction.
Any logic expression can be written in CNF (POS form) and DNF (SOP form).
Any expression in CNF can be converted to DNF and vice versa ( $\Sigma \Pi \leftrightarrow \Pi \Sigma$ ).

## Conventions:

- Write the literals in the terms in alphabetical order: $a b \bar{c}$ (not $a \bar{c} b$ )
- Write the expression starting with the term that has the fewest (or the most) literals, and then proceeding in ascending (or descending) order of literals per term, such as: $a b+a \bar{c} d+a b \bar{c} d$ or $a b \bar{c} d+a \bar{c} d+a b$

Value of a logic expression:
An expression, $E(X)$, generates for each combination of the input vector $X=\left(x_{1}, \ldots . x_{n}\right)$, a value from the set of $B=\{0,1\}$.
These values constitute the truth table for the expression.
Example: $E(X)=x_{1} x_{2}+x_{3}$
Truth table for the expression

$X=$| $x_{1}$ | $x_{2}$ | $x_{3}$ | $E(X)$ |
| :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 0 |
| 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 0 | 1 |
| 1 | 1 | 1 | 1 |



## Digital Circuits

## Applying axioms and theorems to expressions

The axioms and theorems of Boolean algebra defined for binary values are also valid for expressions due to the closure property.
Remember: According to the closure property, the value generated by an expression $E$ is a binary value, i.e., $E(X) \in B=\{0,1\}$
Examples:

$$
E(a, b, c, d)=b \bar{c}+a d+\bar{a} b
$$

Identity: $\quad \mathrm{E}(\mathrm{X})+0=\mathrm{E}(\mathrm{X})$

$$
E(X) \cdot 1=E(X)
$$

$$
\begin{aligned}
\mathrm{E}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d})+0 & =(\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+\overline{\mathrm{a}} \mathrm{~b})+0=\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+(\overline{\mathrm{a}}+0)(\mathrm{b}+0) \\
& =\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+\overline{\mathrm{a}} \mathrm{~b}=\mathrm{E}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d})
\end{aligned}
$$

$$
\begin{aligned}
\mathrm{E}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d}) \cdot 1 & =(\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+\overline{\mathrm{a}} \mathrm{~b}) \cdot 1=\mathrm{b} \overline{\mathrm{c}} 1+\mathrm{ad} 1+\overline{\mathrm{a}} \mathrm{~b} 1 \\
& =\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+\overline{\mathrm{a}} \mathrm{~b}=\mathrm{E}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d})
\end{aligned}
$$

Annihilator (or Dominance): $\quad \mathrm{E}(\mathrm{X})+1=1 \quad \mathrm{E}(\mathrm{X}) \cdot 0=0$

$$
\mathrm{E}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d})+1=(\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+\overline{\mathrm{a}} \mathrm{~b})+1=1
$$

$$
\mathrm{E}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d}) \cdot 0=(\mathrm{b} \overline{\mathrm{c}}+\mathrm{ad}+\overline{\mathrm{a}} \mathrm{~b}) \cdot 0=0
$$

## Digital Circuits

"Order Relation" between binary vectors:
To explain some properties of logic expressions, we can define and use the following two order relations " $<$ " and " $\leq$ ":

1. An order relation "<" between elements of set $B=\{0,1\}: 0<1$

- Read as "0 precedes 1" or "0 is smaller than 1".

2. Another order relation " $\leq "$ between $X \in B^{n}$ vectors can be defined as follows:

- If each component of X 1 is smaller than (precedes) or equal to the component of vector X 2 in the corresponding position, then $\mathrm{X} 1 \leq \mathrm{X} 2$.


## Example:

X1=1001, X2 = 1101
$\mathrm{X} 1 \leq \mathrm{X} 2$.
The order relation " $\leq$ " may not exist between all vectors.
Example: X1 = 0011, X2 = 1001
There is no order relation between X 1 and X 2 (Neither $\mathrm{X} 1 \leq \mathrm{X} 2$ nor $\mathrm{X} 2 \leq \mathrm{X} 1$ is true).

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| :---: | :---: | :---: | :---: | :---: |

## Digital Circuits

Order relation between expressions:
$E(X) \leq F(X)$ denotes that values of $E$ generated by combinations of input $X$ are always smaller than (or equal to) all values of $F$ generated by the same input combinations.


Using the order relation to show absorption properties of expressions
Remember: The theorems given for binary values on slide 2.3 are also valid for expressions due to the closure property.
Consequently, the absorption theorem given for binary values $(a+a \cdot b=a$ and $a \cdot(a+b)=a)$ is also valid for expressions because of the closure property.
However, the order relation $\leq$ makes it easier to grasp the absorption properties of expressions.
Since absorption is an important theorem used to simplify expressions, we will again illustrate this theorem using the order relation $\leq$.
We will consider two cases.

- Case $A$ : Special case: $E(X) \leq F(X)$ (as in the example on slide 2.12)
- Case B: General case: The order relation $\leq$ does not exist between $E(X)$ and $F(X)$


## Digital Circuits

Using the order relation to show absorption properties of expressions (cont'd)
CASE A: Special case: $E(X) \leq F(X)$

## Absorptions:

1. $E(X)+F(X)=F(X)$
2. $E(X) \bullet F(X)=E(X)$

## Example:

Consider the example given on slide 2.12.


Absorption properties (1 and 2) can be seen on the diagram on the right.
Expressions for the functions on slide 2.12:
$\mathrm{E}\left(x_{1}, x_{2}, x_{3}\right)=\overline{x_{1}} \cdot \overline{x_{3}}+x_{1} \cdot x_{2} \cdot x_{3}, \quad \mathrm{~F}\left(x_{1}, x_{2}, x_{3}\right)=\overline{x_{1}} \cdot \overline{x_{3}}+\overline{x_{2}} \cdot \overline{x_{3}}+x_{2} \cdot x_{3}$
Absorptions:
E(X) F(X)

1. $\mathrm{E}(\mathrm{X})+\mathrm{F}(\mathrm{X})=\overline{x_{1}} \cdot \overline{x_{3}}+x_{1} \cdot x_{2} \cdot x_{3}+\overline{x_{1}} \cdot \overline{x_{3}}+\overline{x_{2}} \cdot \overline{x_{3}}+x_{2} \cdot x_{3}$

$$
=\overline{x_{1}} \cdot \overline{x_{3}}+\overline{x_{2}} \cdot \overline{x_{3}}+x_{2} \cdot x_{3}=\mathrm{F}(\mathrm{X})
$$

2. $\mathrm{E}(\mathrm{X}) \bullet \mathrm{F}(\mathrm{X})=\left(\overline{x_{1}} \cdot \overline{x_{3}}+x_{1} \cdot x_{2} \cdot x_{3}\right) \cdot\left(\overline{x_{1}} \cdot \overline{x_{3}}+\overline{x_{2}} \cdot \overline{x_{3}}+x_{2} \cdot x_{3}\right)$
$=\overline{x_{1}} \cdot \overline{x_{3}}+x_{1} \cdot x_{2} \cdot x_{3}=\mathrm{E}(\mathrm{X})$

## Digital Circuits

Using the order relation to show absorption properties of expressions (cont'd) Case B: General case: The order relation $\leq$ does not exist between $E(X)$ and $F(X)$


The following inequalities are always true for any $E$ and $F$ (as can be seen on the diagram on the left):
$\mathrm{E} \cdot \mathrm{F} \leq \mathrm{E} \leq \mathrm{E}+\mathrm{F}$
$\mathrm{E} \cdot \mathrm{F} \leq \mathrm{F} \leq \mathrm{E}+\mathrm{F}$

Absorption properties of expressions follow directly from these inequalities (we can generalize the absorption theorems for Boolean variables and apply them to expressions due to the closure property):
Absorption Properties of expressions:

$$
\begin{gathered}
E+E \cdot F=E \quad \text { dual } \quad E(E+F)=E \\
\text { Proof: } \mathrm{E}(\mathrm{E}+\mathrm{F})=\mathrm{EE}+\mathrm{EF}=\mathrm{E}+\mathrm{EF}=\mathrm{E}(1+\mathrm{F})=\mathrm{E} \\
E+\bar{E} \cdot F=E+F \\
\text { Proof: } E+\bar{E} \cdot F=(E+\bar{E})(E+F)=1(E+F)=E+F \\
\hline E(\bar{E}+F)=E \cdot F \\
\text { dual } \quad
\end{gathered}
$$

These properties are used to minimize (simplify) logic expressions.

## Digital Circuits

## Example (general case):

 The order relation ( $\leq$ ) does not exist between the $E$ and $F$ expressions given in this example. From the truth table, we observe
$E \cdot F=a b c^{\prime} d \quad E+F=a b c^{\prime}+b d$

$$
E \cdot F \leq E \quad \text { and } \quad E \cdot F \leq F \text {. }
$$

We can check the absorption properties:
$\mathrm{E} \cdot \mathrm{F}+\mathrm{E} \stackrel{?}{=} \mathrm{E}$
$a b c^{\prime} d+a b c^{\prime} \stackrel{?}{=} a b c^{\prime}$
and
$\mathrm{E} \cdot \mathrm{F}+\mathrm{F} \stackrel{?}{=} \mathrm{F}$
$a b c^{\prime} d+b d \stackrel{?}{=} b d$
Again, from the truth table, we observe

$$
E \leq E+F \quad \text { and } \quad F \leq E+F .
$$

We can check the absorption properties:

$$
E \cdot(E+F) \stackrel{?}{=} E
$$

$a b c^{\prime}\left(a b c^{\prime}+b d\right) \stackrel{?}{=} a b c^{\prime} \checkmark$ We have thus shown the and
$F \cdot(E+F) \stackrel{?}{=} F$ absorption properties using a truth table instead of axioms.
$b d\left(a b c^{\prime}+b d\right) \stackrel{?}{=} b d \checkmark$
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## Digital Circuits

## The Consensus Theorem (SOP Form)

Assume that $\mathrm{E}_{1}$ and $\mathrm{E}_{2}$ are two expressions that do not contain the literal $\mathrm{x}_{1}$ : $E_{1}\left(x_{2}, \ldots . x_{n}\right)$ and $E_{2}\left(x_{2}, \ldots . x_{m}\right)$
We can create a new expression by multiplying one term by $x_{1}$ and the other one by the complement of $\mathrm{x}_{1}\left(\overline{x_{1}}\right)$.

$$
E=x_{1} E_{1}+\overline{x_{1}} E_{2}
$$

Here, $x_{1}$ is called a biform variable because it appears both positively (as itself: $x_{1}$ ) and negatively (as complement: $\overline{x_{1}}$ ) in the expression.
Examples: $\quad$ a) $x_{1}\left(x_{2}+\overline{x_{3}}\right)+\overline{x_{1}}\left(x_{3}+x_{4}\right)$,
b) $x_{1} x_{2} \overline{x_{3}}+\overline{x_{1}} x_{4}+x_{5}$

- Given a pair of product terms that include a biform variable, the consensus term (SOP) is formed by multiplying (ANDing) the two original terms together, leaving out the selected biform variable and its complement.
- $\mathrm{E}_{1} \mathrm{E}_{2}$ is the consensus term of $x_{1} E_{1}+\overline{x_{1}} E_{2}$ with respect to variable $\mathrm{x}_{1}$.

Example: Consensus of $\boldsymbol{a b c}+\overline{\boldsymbol{a}} \boldsymbol{c \boldsymbol { d }}$ (with respect to a) is bccd $=\boldsymbol{b} \boldsymbol{c} \boldsymbol{d}$.
Theorem: The consensus term is redundant and can be eliminated.

$$
x_{1} E_{1}+\overline{x_{1}} E_{2}+E_{1} E_{2}=x_{1} E_{1}+\overline{x_{1}} E_{2}
$$

## Digital Circuits

The Consensus Theorem (POS Form)
According to the duality principle, the consensus theorem is also valid for expressions written in product-of-sums (POS) form.

Assume that $\mathrm{E}_{1}$ and $\mathrm{E}_{2}$ are two expressions that do not contain the literal $\mathrm{x}_{1}$ :
$\mathrm{E}_{1}\left(\mathrm{x}_{2}, \ldots . \mathrm{x}_{\mathrm{n}}\right)$ and $\mathrm{E}_{2}\left(\mathrm{x}_{2}, \ldots . \mathrm{x}_{\mathrm{m}}\right)$
We can create a new expression by adding $x_{1}$ to one term and the complement of $x_{1}$ to the other one.

$$
E=\left(\boldsymbol{x}_{1}+E_{1}\right)\left(\overline{x_{1}}+E_{2}\right)
$$

Here, $\mathrm{x}_{1}$ is a biform variable.
Examples: a) $\left(x_{1}+x_{2}+\overline{x_{3}}\right)\left(\overline{x_{1}}+x_{3}+x_{4}\right)$,
b) $\left(x_{1}+x_{2} \overline{x_{3}}\right)\left(\overline{x_{1}}+x_{3} x_{4}\right)$

- Given a pair of sums that include a biform variable, the consensus term (POS) is formed by adding (ORing) the two original terms together, leaving out the selected biform variable and its complement.
- $\mathbf{E}_{1}+\mathbf{E}_{2}$ is the consensus term of $\left(x_{1}+E_{1}\right)\left(\overline{x_{1}}+E_{2}\right)$ with respect to the variable $x_{1}$. Example: Consensus of $(\boldsymbol{a}+\boldsymbol{b}+\boldsymbol{c})(\overline{\boldsymbol{a}}+\boldsymbol{c}+\boldsymbol{d})$ is: $b+c+c+d=\boldsymbol{b}+\boldsymbol{c}+\boldsymbol{d}$.

Theorem: The consensus term is redundant and can be eliminated.

$$
\left(x_{1}+E_{1}\right)\left(\overline{x_{1}}+E_{2}\right)\left(\boldsymbol{E}_{1}+\boldsymbol{E}_{2}\right)=\left(x_{1}+E_{1}\right)\left(\overline{x_{1}}+E_{2}\right)
$$

Example: Minimization of a logic expression using the consensus theorem

$$
\begin{aligned}
& \mathbf{F}(\mathbf{A}, \mathbf{B}, \mathbf{C})=\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A B C}+\mathbf{A B} \overline{\mathbf{C}} \\
& =\bar{A} \bar{B} C+\bar{A} B C+A \bar{B} C+A B C+A B \bar{C}+\bar{A} B \\
& \text { Consensus (with respect to C) } \\
& =\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B C} \mathbf{C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A} \boldsymbol{B}^{\prime} \mathbf{C}+\mathbf{A} \mathbf{B}^{\mathbf{C}} \overline{\mathbf{C}}+\mathbf{A B} \\
& =\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B} \mathbf{C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A} \mathbf{B} \\
& =\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B C}+\overline{\mathbf{A}} \mathbf{C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A B} \\
& =\bar{A} \bar{B} C+\bar{A} B / C+\bar{A} C+A \bar{B} C+A B \\
& =\overline{\mathbf{A}} \mathbf{C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A} \mathbf{B} \\
& =\overline{\mathbf{A}} \mathbf{C}+\mathbf{A} \overline{\boldsymbol{B}} \mathbf{C}+\mathbf{A B}+\mathbf{A} \mathbf{C} \quad \text { Consensus (with resp } \\
& =\overline{\mathbf{A}} \mathbf{C}+\mathbf{A B}+\mathbf{A} \mathbf{C} \\
& =\bar{A} C+A B+A C+C \\
& =\mathbf{A B}+\mathbf{C} \\
& \text { is added } \\
& \text { Absorption } \\
& \text { Consensus (with respect to } B \text { ) } \\
& \text { is added } \\
& \text { Absorption } \\
& \text { Absorption } \\
& \text { Consensus (with respect to A) is added } \\
& \text { Absorption }
\end{aligned}
$$

## Logic (Boolean) Functions

Logic functions are defined on the input set $\mathrm{B}^{\mathrm{n}}$ (vectors with n binary variables).
There are three types of logic functions:

1. Simple (basic) functions: Multiple inputs, single output

$$
\begin{aligned}
& y=f(X): B^{n} \rightarrow B \\
& \forall X_{i} \in B^{n} ; \exists!y_{i} \in B ; y=f(X)
\end{aligned}
$$

"For any $X$, there is exactly one $y$ such that $f(X)=y$."
The truth table for $y=f(X)$




## Digital Circuits

2. General functions: Multiple inputs, multiple outputs

$$
Y=f(X): B^{n} \rightarrow B^{m}, \quad X=\left(x_{1}, \ldots x_{n}\right), Y=\left(y_{1}, \ldots y_{m}\right)
$$

## Example:

$\mathrm{Y}=\mathrm{f}(\mathrm{X})$
$\mathrm{X} \in \mathrm{B}^{3}$
$\mathrm{Y} \in \mathrm{B}^{2}$ $\mathrm{X}_{1} \longrightarrow \mathrm{X}_{2} \longrightarrow \mathrm{X}_{3} \longrightarrow \mathrm{y}_{1}$

The truth table for $Y=f(X)$

| $\mathrm{x}_{1}$ | $\mathrm{x}_{2}$ | $x_{3}$ | $\mathrm{y}_{1}$ | $\mathrm{y}_{2}$ |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 1 | 1 |
| 0 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 0 | 0 |
| 0 | 1 | 1 | 0 | 0 |
| 1 | 0 | 0 | 1 | 1 |
| 1 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 1 |
| 1 | 1 | 1 | 1 | 0 |

## Digital Circuits

## 3. Incompletely specified functions:

A large digital system usually consists of many subcircuits.

## Example:

In the following example, the output of logic circuit L1 drives the output of logic circuit L2.


- Let us assume that the output of L1 does not generate all possible combinations of values for $A, B$, and $C$.
- In particular, we will assume that there are no combinations of values for $w, x$, $y$, and $z$, which cause A, B, and C to assume values of 001 and 110.
- In other words, L1 never generates the output combinations 001 and 110.
- Hence, when we design L2, it is not necessary to specify values of $F$ for $A B C=$ 001 or 110 because these combinations of values can never occur as inputs to L2.


## Digital Circuits

3. Incompletely specified functions (cont'd):

- For example, F might be specified by the following table:

- The X's in the table indicate that we do not care whether the value of 0 or 1 is assigned to F for the combinations $\mathrm{ABC}=001$ or 110.
- In this example, we do not care what the value of $F$ is because these input combinations never occur anyway.
- The function $F$ is then incompletely specified.
- The terms $\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}$ and $\mathbf{A} \mathbf{B} \overline{\mathbf{C}}$ are referred to as "don't care" terms because we do not care whether they are present in the function or not.


## Digital Circuits

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3. Incompletely specified functions (cont'd):

- When we realize the function, we must specify values for the don't cares.
- It is desirable to choose values which will help simplify the function.
- For the example on Slide 2.24:
- If we assign the value 0 to both $X^{\prime} s$, then $\mathbf{F}=\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}+\overline{\mathbf{A}} \mathbf{B} \mathbf{C}+\mathbf{A} \mathbf{B} \mathbf{C}=\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}+\mathbf{B} \mathbf{C}$

Finding expression from truth table
See 2.32 canonical forms

- If we assign 0 to the first $X$ and 1 to the second, then $\mathbf{F}=\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}+\overline{\mathbf{A}} \mathbf{B C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}}+\mathbf{A B C}=\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}+\mathbf{B C}+\mathbf{A} \mathbf{B}$
- If we assign 1 to the first $X$ and 0 to the second, then $\mathbf{F}=\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}+\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B} \mathbf{C}+\mathbf{A} \mathbf{B} \mathbf{C}=\overline{\mathbf{A}} \overline{\mathbf{B}}+\mathbf{B} \mathbf{C}$

The third choice of values leads to the simplest solution.

- If we assign 1 to both $X$ 's, then

$$
\mathbf{F}=\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}+\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B} \mathbf{C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}}+\mathbf{A} \mathbf{B} \mathbf{C}=\overline{\mathbf{A}} \overline{\mathbf{B}}+\mathbf{B} \mathbf{C}+\mathbf{A} \mathbf{B}
$$

- In Section 4, we will see the selection of unspecified (don't care) values and the simplification of incompletely specified functions in detail.
- Incompletely specified functions can arise in the following cases:
- Certain combinations of inputs cannot occur (as in the above example).
- All input combinations may occur, but the function output is used in such a way that we do not care whether it is 0 or 1 for certain input combinations.


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## Digital Circuits

3. Incompletely specified functions (cont'd):

Example: A function that increments $B C D$ numbers
We will create a general function to increment $B C D$ numbers given on slide 1.9.
This function will have 4 inputs and 4 outputs because BCD numbers are 4-bit patterns.
Since $B C D$ numbers are represented using binary code words between 0000-1001, combinations between 1010-1111 will never appear as inputs to this function.
Even if these values are applied to the inputs of the function, we do not care what the output values are.

For these input combinations, the output values of the circuit (function) are not specified.
An $X$ or $\Phi$ represents a don' $\dagger$ care.

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## Digital Circuits

## Representation of Logic (Boolean) Functions

There are different ways of representing (expressing) the same logic function. When designing logic circuits, we choose the most suitable representation.

Truth Table Representation:
We write the output values for all possible input combinations (variables) in a table.
We usually write the input columns so that they follow the order of binary counting.
Input variables are encoded as binary numbers.
(See examples in 2.20-2.22)

## Numbered (Indexed) Representation:

Input variables are encoded as binary numbers.
We assign a decimal number for each input combination based on its binary value.
To represent the function, we list the decimal number of each input combination for which the function generates "1" (or logic "0" or " $\Phi$ ").


## Digital Circuits

Example: Representation of a completely specified general logic function:
We apply the numbered representation to all outputs.

| Truth Table: |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: |
| Num. $\mathrm{x}_{1}$ $\mathrm{x}_{2}$ $\mathrm{y}_{1}$ $\mathrm{y}_{2}$ <br> 0 0 0 1 1 <br> 1 0 1 0 1 <br> 2 1 0 1 0 <br> 3 1 1 0 0 |  |  |  |  |

Numbered (Indexed) Representation:

$$
\begin{aligned}
& y_{1}=f\left(x_{1}, x_{2}\right)=U_{1}(0,2) \\
& y_{2}=f\left(x_{1}, x_{2}\right)=U_{1}(0,1)
\end{aligned}
$$

Representation of the same function with "0"-generating combinations:

$$
\begin{aligned}
& y_{1}=f\left(x_{1}, x_{2}\right)=\cup_{0}(1,3) \\
& y_{2}=f\left(x_{1}, x_{2}\right)=\cup_{0}(2,3)
\end{aligned}
$$

## Digital Circuits

Example: Representation of an incompletely specified general logic function:
In this case, writing only 1-generating or only 0-generating input combinations is not sufficient.
We must write at least two of the three groups (1-generating, 0-generating, don't care).

| Truth Table: |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| N | $\mathrm{x}_{1}$ | $\mathrm{x}_{2}$ | $\mathrm{y}_{1}$ | $\mathrm{y}_{2}$ |
| 0 | 0 | 0 | 1 | 1 |
| 1 | 0 | 1 | 0 | $\Phi$ |
| 2 | 1 | 0 | $\Phi$ | 0 |
| 3 | 1 | 1 | 0 | $\Phi$ |

## Numbered (Indexed) Representations:

$$
\begin{array}{ll} 
& y_{1}=f\left(x_{1}, x_{2}\right)=\cup_{1}(0)+\cup_{0}(1,3) \\
\text { or } & y_{1}=f\left(x_{1}, x_{2}\right)=\cup_{1}(0)+\cup_{\Phi}(2) \\
\text { or } & y_{1}=f\left(x_{1}, x_{2}\right)=\cup_{0}(1,3)+\cup_{\Phi}(2) \\
& y_{2}=f\left(x_{1}, x_{2}\right)=\cup_{1}(0)+\cup_{0}(2) \\
\text { or } & y_{2}=f\left(x_{1}, x_{2}\right)=\cup_{1}(0)+\cup_{\Phi}(1,3) \\
\text { or } & y_{2}=f\left(x_{1}, x_{2}\right)=\cup_{0}(2)+\cup_{\Phi}(1,3)
\end{array}
$$

## Digital Circuits

## Algebraic Representation (Expressions) and Canonical Forms

The word description of a real-world logic design problem can be translated into a truth table.
Example: Assume that input variable A represents the phrase "the car door is open" and B represents "the key is inserted",
then the truth table can specify the action $\mathbf{Z}$ to be taken, where $\mathbf{Z}=1$ means that the alarm sounds.

| Num. | A | B | Z | Truth tables of real-world logic design problems are more |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 | complicated. |
| 1 | 0 | 1 | 0 | To handle a logic design problem and implement the solution |
| 2 | 1 | 0 | 0 | using logic gates, we need to obtain an algebraic expression |
| 3 | 1 | 1 | 1 | for the output function. $Z=f(A, B)$ |

Logic expressions of the Boolean functions can be obtained in canonical forms from their truth tables.
There are two types of canonical forms:

- 1st canonical form: SOP (ГП) form. Example: $\boldsymbol{a} \boldsymbol{b} \boldsymbol{c}+\boldsymbol{a} \overline{\boldsymbol{b}} \boldsymbol{c}$ The sum of products, each of which corresponds to a "1"-generating combination.
- 2nd canonical form: POS ( $\Pi \Sigma)$ form. Example: $(\boldsymbol{a}+\boldsymbol{b}+\overline{\boldsymbol{c}})(\boldsymbol{a}+\overline{\boldsymbol{b}}+\overline{\boldsymbol{c}})$

The product of sums, each of which corresponds to a "0"-generating combination.
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## 1st Canonical Form: Sum of Products

- The 1st canonical form is the sum of special products called minterms.
- Minterm: For a Boolean function of $n$ variables, a product of $n$ literals in which each variable appears exactly once (in either true or complemented form, but not both) is called a minterm.
For example, a function with 3 variables ( $a, b, c$ ) has 8 minterms:

$$
\overline{\mathbf{a}} \overline{\mathbf{b}} \overline{\mathbf{c}}, \overline{\mathbf{a}} \overline{\mathbf{b}} \mathbf{c}, \overline{\mathbf{a}} \overline{\mathbf{b}}, \overline{\mathbf{a}} \mathbf{b} \mathbf{c}, \mathbf{a} \overline{\mathbf{b}} \overline{\mathbf{c}}, \mathbf{a} \overline{\mathbf{b}} \mathbf{c}, \mathbf{a} \mathbf{b} \overline{\mathbf{c}}, \mathbf{a} \mathbf{b} \mathbf{c}
$$

- Each minterm that appears in the 1st canonical form covers only one row of the truth table with the output "1".
For example, the minterm $\overline{\mathbf{a}} \overline{\mathbf{b}} \overline{\mathbf{c}}$ can generate "1" only for the input value $\mathbf{a b c}=000$.
For all other input combinations, the minterm $\overline{\mathbf{a}} \overline{\mathbf{b}} \overline{\mathbf{c}}$ generates " 0 ".


## Summary:

- The 1st canonical form of a function is the sum of minterms.
- In the 1st canonical form, each product (minterm) in the sum corresponds to a row in the truth table with the output "1".


## Digital Circuits

## 1st Canonical Form (cont'd) :

Finding minterms:
Truth Table $\rightarrow$ Expression in SOP form

- To find minterms, we locate all rows in the truth table where the output is "1".
- To obtain the individual minterms, we substitute variables (for example, $\mathrm{A}, \mathrm{B}$, or C) for ones (of the inputs) and complements of variables ( $\overline{\mathbf{A}}, \overline{\mathbf{B}}$, or $\overline{\mathbf{C}}$ ) for zeros in the truth table.


## Example:

"True" (1) combinations: $001 \quad 011 \quad 101 \quad 110111$



## Digital Circuits

## The 1st canonical form of the complement of a function:

We can similarly obtain the 1st canonical form of the complement of a function by considering the "false" ( 0 ) rows.

## Example:

Obtain the 1st canonical form of the complement of a function F from the previous example.

1st canonical form of $\overline{\boldsymbol{F}}$ :

$$
\overline{F(A, B, C)}=\bar{A} \bar{B} \bar{C}+\bar{A} B \bar{C}+A \bar{B} \bar{C}
$$

| A | $\mathbf{B}$ | C | $\mathbf{F}$ | $\overline{\mathbf{F}}$ |
| :--- | :--- | :--- | :--- | :--- |
| 0 | 0 | 0 | 0 | $1 \ldots$ |
| 0 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 0 | 1 |
| 0 | 1 | 1 | 1 | 0 |
| 1 | 0 | 0 | 0 | 1 |
| 1 | 0 | 1 | 1 | 0 |
| 1 | 1 | 0 | 1 | 0 |
| 1 | 1 | 1 | 1 | 0 |

## Digital Circuits

## Simplification of expressions in the 1st canonical form:

Canonical forms are usually not the simplest (optimal) algebraic expression of the function.
They can usually be simplified.

## Minimization:

$$
\begin{aligned}
\mathbf{F}(\mathbf{A}, \mathbf{B}, \mathbf{C}) & =\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B} \mathbf{C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A} \mathbf{B} \mathbf{C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}} \\
& =(\overline{\mathbf{A}} \overline{\mathbf{B}}+\overline{\mathbf{A}} \mathbf{B}+\mathbf{A} \overline{\mathbf{B}}+\mathbf{A} \mathbf{B}) \mathbf{C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}} \\
& =(\overline{\mathbf{A}}+\mathbf{A})(\overline{\mathbf{B}}+\mathbf{B}) \mathbf{C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}} \\
& =\mathbf{C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}} \\
& =\mathbf{A} \mathbf{B} \overline{\mathbf{C}}+\mathbf{C} \\
& =\mathbf{A} \mathbf{B}+\mathbf{C}
\end{aligned}
$$

- A Boolean function may have many possible logic expressions. They produce the same result given the same inputs.
- Since the minterms in the 1st canonical form are in one-to-one correspondence with the 1's of the truth table, the 1st canonical (standard) form expression for a function is unique.
- A function can have only one expression in the 1st canonical form.


## Digital Circuits

## Indexing minterms:

We assign each minterm an index (number) based on the binary encoding of the variables.
This decimal number represents the row number (Row numbers start at 0 ).
For example, we assign the index 5 to the minterm $\mathbf{A} \overline{\mathbf{B}} \mathbf{C}$ (101) and designate it m5.
Minterms for 3 variables ( $A, B, C$ ):

| Inputs: |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| A | B | C | Minte | rms |
| 0 | 0 | 0 | $\overline{\mathbf{A}} \overline{\mathbf{B}} \overline{\mathbf{C}}$ | $=\mathrm{m} 0$ |
| 0 | 0 | 1 | $\overline{\mathrm{A}} \overline{\mathrm{B}} \mathrm{C}$ | $=\mathrm{m} 1$ |
| 0 | 1 | 0 | $\overline{\text { A B C }}$ | = m 2 |
| 0 | 1 | 1 | $\overline{\text { A B C }}$ | $=\mathrm{m} 3$ |
| 1 | 0 | 0 | A $\overline{\mathbf{B}} \overline{\mathrm{C}}$ | $=\mathrm{m} 4$ |
| 1 | 0 | 1 | A $\overline{\mathrm{B}} \mathrm{C}$ | = m5 |
| 1 | 1 | 0 | A B $\overline{\mathrm{C}}$ | = m6 |
| 1 | 1 | 1 | ABC | $=\mathrm{m} 7$ |

Example:
Expression of function $F$ in (2.33) in 1st canonical form:

$$
\mathrm{F}(\mathrm{~A}, \mathrm{~B}, \mathrm{C})=\Sigma \mathrm{m}(1,3,5,6,7)
$$

$$
=m 1+m 3+m 5+m 6+m 7
$$

$$
=\overline{\mathbf{A}} \overline{\mathbf{B}} \mathbf{C}+\overline{\mathbf{A}} \mathbf{B} \mathbf{C}+\mathbf{A} \overline{\mathbf{B}} \mathbf{C}+\mathbf{A} \mathbf{B} \overline{\mathbf{C}}+\mathbf{A} \mathbf{B} \mathbf{C}
$$

$F=\Sigma_{A, B, C}(1,3,5,6,7)$ (Sum of minterms)

## 2nd Canonical Form: Product of Sums

- The 2nd canonical form is the product of special sum terms called maxterms.
- Maxterm: For a Boolean function of $n$ variables, a sum of $n$ literals in which each variable appears exactly once (in either true or complemented form, but not both) is called a maxterm.
- For example, a function with 3 variables ( $a, b, c$ ) has 8 maxterms:
$\mathbf{a}+\mathbf{b}+\mathbf{c}, \mathbf{a}+\mathbf{b}+\overline{\mathbf{c}}, \mathbf{a}+\overline{\mathbf{b}}+\mathbf{c}, \mathbf{a}+\overline{\mathbf{b}}+\overline{\mathbf{c}}, \overline{\mathbf{a}}+\mathbf{b}+\mathbf{c}, \overline{\mathbf{a}}+\mathbf{b}+\overline{\mathbf{c}}, \overline{\mathbf{a}}+\overline{\mathbf{b}}+\mathbf{c}, \overline{\mathbf{a}}+\overline{\mathbf{b}}+\overline{\mathbf{c}}$
- Each maxterm has a value of "0" for exactly one combination of values for the input variables and "1" for all other combinations.
For example, the maxterm $\mathbf{a}+\mathbf{b}+\mathbf{c}$ can generate " 0 " only for the input value abce000.
For all other input combinations, the maxterm $\mathbf{a}+\mathbf{b}+\mathbf{c}$ generates " 1 ".


## Summary:

- The 2nd canonical form of a function is the product of maxterms.
- In the 2nd canonical form, each sum term (maxterm) in the product corresponds to a row in the truth table with the output " 0 ".


## Digital Circuits

## Finding maxterms:

2nd Canonical Form (cont'd):
Truth Table $\rightarrow$ Expression in POS form

- To find the maxterms, we locate all rows in the truth table where the output is " 0 ".
- To obtain the individual maxterms, we substitute variables (for example, A, B, or C) for zeros (of the inputs) and complements of variables ( $\overline{\mathbf{A}}, \overline{\mathbf{B}}$, or $\overline{\mathbf{C}}$ ) for ones in the truth table.


## Example:

"False" (0-generating) combinations: 000010100
Product of maxterms: $\quad \mathrm{F}(\mathrm{A}, \mathrm{B}, \mathrm{C})=(\mathbf{A}+\mathbf{B}+\mathbf{C})(\mathbf{A}+\overline{\mathbf{B}}+\mathbf{C})(\overline{\mathbf{A}}+\mathbf{B}+\mathbf{C})$


Note that this function F has the same truth table as the function on slide 2.33. The expressions in the 1st and 2nd canonical forms both correspond to the same truth table.
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## Digital Circuits

The 2nd canonical form of the complement of a function:
We can similarly obtain the 2nd canonical form of the complement of a function by considering the "true" (1) rows:

## Example:

Obtain the 2nd canonical form of the complement of a function F from the previous example.

2nd canonical form of $\bar{F}$ :


## Digital Circuits

## Simplification of expressions in the 2nd canonical form:

Canonical forms are usually not the simplest (optimal) algebraic expression of the function.
They can usually be simplified.

## Minimization

$$
\begin{aligned}
\mathbf{F}(\mathbf{A}, \mathbf{B}, \mathbf{C}) & =(\mathbf{A}+\mathbf{B}+\mathbf{C})(\mathbf{A}+\overline{\mathbf{B}}+\mathbf{C})(\overline{\mathbf{A}}+\mathbf{B}+\mathbf{C}) \\
& =((\mathbf{A}+\mathbf{C})(\mathbf{B}+\overline{\mathbf{B}}))(\overline{\mathbf{A}}+\mathbf{B}+\mathbf{C}) \\
& =(\mathbf{A}+\mathbf{C})(\overline{\mathbf{A}}+\mathbf{B}+\mathbf{C}) \\
& =(\mathbf{A}+\mathbf{C})(\overline{\mathbf{A}}+\mathbf{B}+\mathbf{C})(\mathbf{B}+\mathbf{C}) \text { (consensus) } \\
& =(\mathbf{A}+\mathbf{C})(\mathbf{B}+\mathbf{C})
\end{aligned}
$$

- A Boolean function may have many possible logic expressions. They produce the same result given the same inputs.
- Since the maxterms in the 2nd canonical form are in one-to-one correspondence with the 0's of the truth table, the 2nd canonical (standard) form expression for a function is unique.
- A function can have only one expression in the 2nd canonical form.

| Digital Circuits |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Indexing maxterms: |  |  |  |  |  |  |  |  |
| We assign each maxterm an index (number) based on the binary encoding of the variables. This is a decimal number that represents the row number (Row numbers start at 0). |  |  |  |  |  |  |  |  |
| For example, we assign the index 5 to the maxterm $\overline{\mathbf{a}}+\mathbf{b}+\overline{\mathbf{c}}$ (101) and designate it M5 |  |  |  |  |  |  |  |  |
| Maxterms for 3 variables (A,B,C): |  |  |  |  |  |  |  |  |
| Inputs: |  |  |  |  |  |  |  |  |
| A | B | C | Maxterms |  |  |  |  |  |
| 0 | 0 | 0 | A+B+C | M0 |  |  |  |  |
| 0 | 0 | 1 | $\mathbf{A}+\mathbf{B}+\overline{\mathbf{C}}$ |  | Example: |  |  |  |
| 0 | 1 | 0 | $\mathbf{A}+\overline{\mathbf{B}}+\mathbf{C}$ | M2 | Expression of functio | $F$ in (2. | 38) in 2nd cand | nical |
|  | 1 | 1 | $\underline{\mathbf{A}}+\overline{\mathbf{B}}+\overline{\mathbf{C}}$ | M3 | form: |  | so) |  |
| 1 | 0 | 0 | $\overline{\mathbf{A}}+\mathbf{B + C}$ |  | $\mathbf{F}(\mathbf{A}, \mathbf{B}, \mathbf{C})=\Pi М(0,2$ |  |  |  |
| 1 | 0 |  | $\overline{\mathbf{A}}+\mathbf{B}+\overline{\mathbf{C}}$ |  | $=M O \cdot M$ | $2 \cdot \mathrm{M} 4$ |  |  |
|  |  | 0 | $\overline{\mathbf{A}}+\overline{\mathbf{B}}+\mathbf{C}$ $\overline{\mathbf{A}}+\overline{\mathbf{B}}+\overline{\mathbf{C}}$ |  | $=(\mathbf{A}+\mathbf{B}$ | + C) $(\mathbf{A}+$ | $\overline{\mathbf{B}}+\mathbf{C})(\overline{\mathbf{A}}+$ |  |
|  |  |  |  |  | $\mathrm{F}=\Pi_{\mathrm{A}, \mathrm{B}, \mathrm{C}}$ | ,2,4) prod | duct of max |  |
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## Digital Circuits

## Conversions Between Canonical Forms

- Converting from 1st (sum of minterms) form to 2nd (product of maxterms) form:
- Replace the minterms with maxterms, and assign them numbers of minterms that do not appear in the 1st canonical form.
- Example: $F(A, B, C)=\Sigma m(1,3,5,6,7)=\Pi M(0,2,4)$

$$
\mathrm{F}(\mathrm{~A}, \mathrm{~B}, \mathrm{C})=\mathrm{m} 1+\mathrm{m} 3+\mathrm{m} 5+\mathrm{m} 6+\mathrm{m} 7=\mathrm{M} 0 \cdot \mathrm{M} 2 \cdot \mathrm{M} 4
$$

$\mathrm{F}(\mathrm{A}, \mathrm{B}, \mathrm{C})=\overline{\mathrm{A}} \overline{\mathrm{B}} \mathrm{C}+\overline{\mathrm{A}} \mathrm{BC}+\mathrm{A} \overline{\mathrm{B}} \mathrm{C}+\mathrm{AB} \overline{\mathrm{C}}+\mathrm{ABC}=(\mathrm{A}+\mathrm{B}+\mathrm{C})(\mathrm{A}+\overline{\mathrm{B}}+\mathrm{C})(\overline{\mathrm{A}}+\mathrm{B}+\mathrm{C})$

- Converting from 2nd (product to maxterms) form to 1st (sum of minterms) form:
- Replace the maxterms with minterms, and assign them numbers of maxterms that do not appear in the 2nd canonical form.
- Example: $\mathrm{F}(\mathrm{A}, \mathrm{B}, \mathrm{C})=\Pi \mathrm{M}(0,2,4)=\Sigma \mathrm{m}(1,3,5,6,7)$
- Finding the complement of the function in sum of minterms form:
- Select the minterms that do not appear in the 1st canonical form.
- Example: $F(A, B, C)=\Sigma m(1,3,5,6,7) \quad \Rightarrow \quad \bar{F}(A, B, C)=\Sigma m(0,2,4)$
- Finding the complement of the function in the product of maxterms form:
- Select the maxterms that do not appear in the 2nd canonical form.
- Example: $F(A, B, C)=\Pi M(0,2,4) \Rightarrow \overline{F(A, B, C)}=\Pi M(1,3,5,6,7)$


## Digital Circuits

Canonical Forms and the De Morgan's Theorem

- Applying De Morgan's law to the complement of the function in the 1st canonical form generates the expression in the 2nd canonical form.


## Example:

Complement of the function in SOP form (Slide 2.34):

$$
\overline{\mathrm{F}}=\overline{\mathrm{A}} \overline{\mathrm{~B}} \overline{\mathrm{C}}+\overline{\mathrm{A}} \mathrm{~B} \overline{\mathrm{C}}+\mathrm{A} \overline{\mathrm{~B}} \overline{\mathrm{C}}
$$

Applying De Morgan

$$
\begin{aligned}
& \overline{\overline{\mathrm{F}}}=\overline{\overline{\mathrm{A}} \overline{\mathrm{~B}} \overline{\mathrm{C}}+\overline{\mathrm{A}} \mathrm{~B} \overline{\mathrm{C}}+\mathrm{A} \overline{\mathrm{~B}} \overline{\mathrm{C}}} \\
& \mathrm{~F}=(\mathrm{A}+\mathrm{B}+\mathrm{C})(\mathrm{A}+\overline{\mathrm{B}}+\mathrm{C})(\overline{\mathrm{A}}+\mathrm{B}+\mathrm{C}) \quad \text { 2nd canonical form (2.38). }
\end{aligned}
$$

- Applying the De Morgan's theorem to the complement of the function in the 2nd canonical form generates the expression in the 1st canonical form.


## Example:

Complement of the function in POS form (2.39):

$$
\overline{\mathrm{F}}=(\mathrm{A}+\mathrm{B}+\overline{\mathrm{C}})(\mathrm{A}+\overline{\mathrm{B}}+\overline{\mathrm{C}})(\overline{\mathrm{A}}+\mathrm{B}+\overline{\mathrm{C}})(\overline{\mathrm{A}}+\overline{\mathrm{B}}+\mathrm{C})(\overline{\mathrm{A}}+\overline{\mathrm{B}}+\overline{\mathrm{C}})
$$

Applying De Morgan

$$
\begin{align*}
& \overline{\bar{F}}=\overline{(A+B+\bar{C}})(A+\overline{\mathrm{B}}+\overline{\mathrm{C}})(\overline{\mathrm{A}}+\mathrm{B}+\overline{\mathrm{C}})(\overline{\mathrm{A}}+\overline{\mathrm{B}}+\mathrm{C})(\overline{\mathrm{A}}+\overline{\mathrm{B}}+\overline{\mathrm{C}}) \\
& \mathrm{F}=\overline{\mathrm{A}} \overline{\mathrm{~B}} \mathrm{C}+\overline{\mathrm{A}} \mathrm{BC}+\mathrm{A} \overline{\mathrm{~B}} \mathrm{C}+\mathrm{AB} \overline{\mathrm{C}}+\mathrm{A} \mathrm{BC}  \tag{2.33}\\
& \text { 1st canonical }
\end{align*}
$$

## Graphical Representation

The input variables (combinations) of a logic function are elements (vectors) of the set $\mathrm{B}^{n}$. Example: $\mathrm{B}^{3}=\{000,001, \ldots, 111\}$
We can represent these variables as vertices of an n-dimensional hypercube. Vertices that correspond to 1-generating combinations are colored (marked). The number of inputs of the function determines how many dimensions the hypercube has.
n -bit input $\rightarrow \mathrm{n}$-dimensional hypercube



## Digital Circuits <br> Karnaugh Maps <br> Maurice Karnaugh (1924-2022), American physicist and mathematician

The Karnaugh map is a tool for representing and simplifying Boolean functions. The Boolean variables and related output values are transferred (generally from a truth table) into a table that is in a matrix form.

Example: $\mathrm{F}(\mathrm{A}, \mathrm{B})$


## Digital Circuits <br> Karnaugh map (cont'd)

The rows and columns are labeled according to the Gray code property so that variables in adjacent squares (horizontal and vertical) of the map differ in only one variable.
Format of the Karnaugh map for a function with 3 inputs: $F(A, B, C)$


Example:

| Num | A | B | C | F |
| :--- | :--- | :--- | :--- | :---: |
| 0 | 0 | 0 | 0 | $\mathbf{0}$ |
| 1 | 0 | 0 | 1 | $\mathbf{0}$ |
| 2 | 0 | 1 | 0 | $\mathbf{0}$ |
| 3 | 0 | 1 | 1 | $\mathbf{1}$ |
| 4 | 1 | 0 | 0 | $\mathbf{0}$ |
| 5 | 1 | 0 | 1 | $\mathbf{1}$ |
| 6 | 1 | 1 | 0 | $\mathbf{1}$ |
| 7 | 1 | 1 | 1 | $\mathbf{1}$ |

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## Digital Circuits

Format of the Karnaugh map of a function with 4 inputs: $F(A, B, C, D)$


## Example:

Draw the Karnaugh map for the following function.
$F(A, B, C, D)=\cup_{1}(0,2,5,8,9,10,11,12,13,14,15)$


We will use Karnaugh maps to simplify Boolean functions in the coming lectures.

