

## Sınav Konuları ve Referans Kitaplar

1. Design of Latch and Flip-Flops
2. Synchronous Sequential Circuit Design
3. State Reduction and Assignment
4. Register Transfer Level & Design with ASM
5. Simple processor design
6. Asynchronous Sequential Digital Circuit Design

### Textbooks

1. M. Morris Mano and Michael D. Ciletti “Digital Design”, Fifth Edition, Prentice Hall, 2013.
2. Frank Vahid and Roman Lysecky ”Verilog for Digital Design”, Wiley 2007.
3. [http://www2.ece.ohio-state.edu/~degroat/ece3561\\_web\\_page.htm](http://www2.ece.ohio-state.edu/~degroat/ece3561_web_page.htm)
4. Randy H. Katz, Gaetano Borriello, “Contemporary Logic Design (2nd Edition)”, Pearson
5. Frank Vahid, “Digital Design”, 1st Edition, Wiley
6. JensSparsø, “AsynchronousCircuitDesign”, A Tutorial
7. J. Sparsø and S. Furber (eds.), “Principles of asynchronous circuit design-A systems perspective”. KluwerAcademicPublishers, 2001. 337 pages.